

PATCH ANTENNA

BACKGROUND OF THE INVENTION

1. Field of the Invention

[0001] The present invention relates to an antenna, and in particular to a patch antenna employed in an electronic device.

2. Description of the Prior Art or Related Art

[0002] In recent years, Wireless Local Area Network(WLAN) products under IEEE 802.11b standard, such as WLAN cards for computers and WLAN Access Points(APs) are gaining popularity in wireless communication market. Now WLAN cards under IEEE 802.11g standard (2.4-2.5 GHz) have introduced into broadband access application. These cards benefit from high gain antennas.

[0003] A communication device often employs a microstrip antenna for its low profile and low cost. One drawback of the traditional microstrip antenna is that it provides a very limited bandwidth which typically is 2-5 percent of the center frequency. Typically, the microstrip antenna can achieve limited bandwidth improvement by increasing the height of the physical space that exists between the antenna's radiating element and the ground plane. Unfortunately, as the height increases, the antenna's inductance also increases, which causes an impedance mismatch between the microstrip antenna and its feeder cable. The impedance mismatch will cause a portion of the feed power to be reflected to the signal source rather than to be radiated to the free space. The greater this reflected feed power, the less power that is radiated from the antenna, thus reducing the gain of the microstrip antenna. So the gain of the microstrip antenna is sacrificed to achieve wider bandwidth in such resolution. Another drawback is that a dielectric layer of the traditional microstrip antenna will introduce insertion loss (typically more than 2 dB), which does not fit a high gain application.

[0004] To achieve a wider bandwidth without expense of antenna's impedance mismatch, US. Pat. No. 4,605,933 discloses a broadband microstrip antenna with a high antenna gain. This microstrip antenna 10 comprises a circular radiating element 20 parallelly spaced from a lower ground plane 12 by an air gap. An upper ground 14 plane is shorted to the lower one 12, A thin dielectric layer 18 is disposed on the upper ground plane 14. A feeder line 30 and a coaxial-to-microstrip launch 28 are disposed on the upper ground plane 14. This microstrip antenna uses air gap as dielectric layer to improve antenna gain and two parallel ground plane to enhance antenna bandwidth. A match tab 24 extending from the lower ground plane 12 couples with the radiating element 20 to get a desired antenna impedance matching. A match strap 32 is disposed between the feed cable 30 and the coaxial-to-microstrip launch 28 for the microstrip antenna impedance matching.

[0005] However, when manufacturing this microstrip antenna, the match tab must be fabricated and mounted individually, which will add complex to manufacturing process. Furthermore, using two match elements increases cost of the microstrip antenna.

[0006] Hence, an improved antenna assembly is desired to overcome the above-mentioned disadvantages of the prior and related arts.

BRIEF SUMMARY OF THE INVENTION

[0007] A primary object of the present invention is to provide a patch antenna having a simple structure while having a good antenna performance.

[0008] A patch antenna for an electronic device comprises a planar metal sheet comprising a first element, a second element and a connecting patch connecting the first element with the second element. A first ground plane is disposed adjacent to the first element. A second ground plane is parallelly spaced from the metal sheet.

A shorted patch shorts the first ground plane to the second ground plane. A plurality of dielectric support-sticks are disposed between the metal sheet and the second ground plane for supporting the metal sheet. A feeder cable comprises an inner conductor electrically connecting with the first element and an outer shield conductor electrically connecting with the first ground plane.

[0009] Other objects, advantages and novel features of the invention will become more apparent from the following detailed description of a preferred embodiment when taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] Fig. 1 is a side view of a preferred embodiment of a patch antenna in accordance with the present invention.

[0011] Fig. 2 illustrates some dimensions of the patch antenna of Fig. 1.

[0012] Fig. 3 is a top view of the patch antenna of Fig. 1.

[0013] Fig. 4 is illustrates some dimensions of the patch antenna of Fig. 3.

[0014] Fig. 5 is the simulated result with Agilent ADS software for the patch antenna of Fig. 1, showing Voltage Standing Wave Ratio (VSWR) as a function of frequency.

DETAILED DESCRIPTION OF THE INVENTION

[0015] Reference will now be made in detail to a preferred embodiment of the present invention.

[0016] Referring to Fig. 1 and Fig. 3, a patch antenna 1 in accordance with the present invention received in a plastic spacer (not shown) comprises a radiating element 2, a match tab 30, an upper ground plane 42 coplanar with the radiating element 2, a lower ground plane 40, a plurality of dielectric support-sticks 6 and a coaxial feeder cable 50.

[0017] The lower and upper ground plane 40, 42 are parallel to each other and shorted by a shorting portion 41. The shorting portion 41, the lower and upper ground planes 40, 42 are all rectangular and integrally formed by a metal sheet. The shorting portion 41 extends perpendicularly from a left edge of the lower ground plane to a left edge of the upper ground plane 42. The upper ground plane 42 is disposed above the left portion of the lower ground plane 40 with a predetermined distance. The lower ground plane 40 is much larger than the upper one 42. The distance between the two ground planes 40, 42 is predetermined due to the requirement of the bandwidth and gain of the patch antenna 1. The distance between the two ground planes 40, 42 increases, the bandwidth of the patch antenna 1 will also increase. But the increasing distance introduces impedance mismatch. Thus a matching element is needed.

[0018] A connecting portion 31, which has a characteristic impedance same as the input impedance of the radiating element 2, connects the match tab 30 with the radiating element 2. The match tab 30 is adjacent to the upper ground plane 42 with a predetermined distance due to the impedance matching between radiating element 2 and the feeder cable 50. The match tab 30, the connecting portion 31 and the radiating element 2 are all rectangular and formed by a flat metal patch. The flat metal patch is coplanar with the upper ground plane 42 and is supported by a plurality of plastic sticks 6 extending from the lower ground plane 40.

[0019] The feeder cable 50 is a coaxial cable. A top portion of the feeder cable 50 is coplanarly disposed on the upper ground plane 42. Portions of an outer dielectric layer and a shield conductor 51 are stripped to expose the inner dielectric layer. A little portion of the inner dielectric layer is stripped to expose an inner conductor 52. The inner conductor 52 extends outwardly to electrically connect with the match tab 30. The outer shield conductor 51 is electrically connected with the upper ground plane 42.

[0020] Fig. 5 shows the simulated result of Voltage Standing Wave Ratio (VSWR) of the patch antenna 1 as a function of frequency. Note that VSWR drops below the desirable maximum value “2” in the 2.37-2.52 GHz frequency band, indicating acceptably efficient operation in the frequency band.

[0021] Reference to Fig. 5, the bandwidth of the patch antenna 1 is 150 MHz which covers the bandwidth under IEEE 802.11b/g standards. The bandwidth is approximate 7 percent of the center frequency. In this embodiment, the antenna gain is more than 9 dB, which is not at expense of bandwidth but by adding the match tab 30 when manufacturing the radiating element 2.

[0022] Referring to Fig. 2 and Fig. 4, major dimensions of the patch antenna 1 are labeled thereon, wherein all dimensions are measured in millimeters (mm).

[0023] In another embodiment, a dielectric substrate may be inserted between the radiating element 2 and the lower ground plane 40 to reduce the sizes of the patch antenna 1 for the special application, such as being a built-in antenna. The distance between radiating element 2 and the lower ground plane 41 can be increased to enhance the bandwidth of the patch antenna if a very high antenna gain is not necessary.

[0024] It is to be understood, however, that even though numerous characteristics and advantages of the present invention have been set forth in the foregoing description, together with details of the structure and function of the invention, the disclosure is illustrative only, and changes may be made in detail, especially in matters of shape, size, and arrangement of parts within the principles of the invention to the full extent indicated by the broad general meaning of the terms in which the appended claims are expressed.